

ABSTRACT

The replica signals generated in a replica generation circuit 306 are sent to a combining circuit 203 through a bus 204. In the combining circuit 203, the above replica signals are input from each circuit board X through Z for combining of replica signals. In a channel allocation control circuit 202, a new channel is allocated so that the relations between the order and likelihood are almost uniform among subsets, based on likelihood information reported from each circuit board X through Z which is a subset; accommodated symbol rates and services (voice signals and packet signals); target SIRs and so on. Thereafter, the allocation control signals are sent to each circuit board X through Z.

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